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L1	51	furukawa-hideyuki.in.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:42
L2	2	furukawa-hideyuki.in. and ((error near2 correct\$4) or ECC)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:43
L3	1	furukawa-hideyuki.in. and ((error\$1 or fault\$1 or defect43) near2 detect\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:44
L5	303184	(detect\$5 or correct\$4) near3 (error\$1 or fault\$1 or defect\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:48
L6	1504	(detect\$5 or correct\$4) near3 (error\$1 or fault\$1 or defect\$3) and (writ\$3 near3 (ECC or (error adj2 correct\$4 adj2 code\$1)))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:50
L7	42	(writ\$3 near3 (ECC or (error adj2 correct\$4 adj2 code\$1))) and (detect\$5 near3 (defective or faulty) near2 block\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:51
L8	53	(writ\$3 near3 (ECC or (error adj2 correct\$4 adj2 code\$1))) and (detect\$5 near3 (defective or faulty) near2 (block\$1 or sectors or area))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:51
L9	27	(writ\$3 near3 (ECC or (error adj2 correct\$4 adj2 code\$1))) and (detect\$5 near3 (defective or faulty) near2 (block\$1 or sectors or area)) and (suspend\$4 or interrupt\$4 or ceas\$4 or stop\$4) near3 (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:53
L11	27	(writ\$3 near3 (ECC or (error adj2 correct\$4 adj2 code\$1))) and (detect\$5 near3 (defective or faulty) near2 (block\$1 or sectors or area)) and (suspend\$4 or interrupt\$4 or ceas\$4 or stop\$4) near3 (ECC or (error adj2 correct\$3 adj2 code\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:55
L12	161	(suspend\$4 or interrupt\$4 or ceas\$4 or stop\$4) near3 (ECC or (error adj2 correct\$3 adj2 code\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:55
L13	8	suspend\$4 near3 (ECC or (error adj2 correct\$3 adj2 code\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:56

L14	11	suspen\$4 near3 (ECC or (error adj2 correct\$3 adj2 code\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:58
L15	0	suspen\$4 near3 (ECC or (error adj2 correct\$3 adj2 code\$1)) and (mark\$3 or detect\$3) adj2 (defective or faulty) adj2 (block\$1 or area)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 14:59
L16	0	suspen\$4 near3 (ECC or (error adj2 correct\$3 adj2 code\$1)) and ((mark\$3 or detect\$3) adj2 (defective or faulty) adj2 (block\$1 or area))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 15:00
L17	0	suspen\$4 near3 (ECC or (error adj2 correct\$3 adj2 code\$1)) and ((mark\$3 or detect\$3) adj2 (defective or faulty) adj2 (block\$1 or area or portion or sector))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/24 15:00


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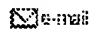
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Fujiwara, E.; Tanaka, M.;

Defect and Fault Tolerance in VLSI Systems, 1993., The IEEE International Workshop 27-29 Oct. 1993 Page(s):183 - 190

[AbstractPlus](#) | Full Text: [PDF\(372 KB\)](#) IEEE CNF2. **Defect-tolerant cache memory design**

Lamet, D.; Frenzel, J.F.;

VLSI Test Symposium, 1993. Digest of Papers., Eleventh Annual 1993 IEEE 6-8 April 1993 Page(s):159 - 163

[AbstractPlus](#) | Full Text: [PDF\(288 KB\)](#) IEEE CNF3. **Cache memory organization to enhance the yield of high performance VLSI proc**

Sohi, G.S.;

Computers, IEEE Transactions on

Volume 38, Issue 4, April 1989 Page(s):484 - 492

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Ferreira, N.C.; Trebossen, R.; Gregoire, M.-C.; Bendriem, B.;

Nuclear Science, IEEE Transactions on

Volume 46, Issue 4, Aug. 1999 Page(s):1062 - 1069

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(676 KB\)](#) IEEE JNL5. **Influence of malfunctioning block detectors on the calculation of single detector PET**

Ferreira, N.C.; Trebossen, R.; Gregoire, M.-C.; Bendriem, B.;

Nuclear Science Symposium, 1998. Conference Record. 1998 IEEE

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| <input type="checkbox"/> | <p><b>4. Throughput analysis method for hybrid ARQ schemes over burst error channels</b><br/> Sato, T.; Kawabe, M.; Kato, T.; Fukasawa, A.;<br/> Vehicular Technology, IEEE Transactions on<br/> Volume 42, Issue 1, Feb. 1993 Page(s):110 - 118<br/> <a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(668 KB)</a> IEEE JNL</p>  |
| <input type="checkbox"/> | <p><b>5. High code rate error correction code design for partial response systems</b><br/> Miller, J.; Wolf, J.K.;<br/> Magnetics, IEEE Transactions on<br/> Volume 37, Issue 2, March 2001 Page(s):704 - 707<br/> <a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(76 KB)</a> IEEE JNL</p>  |
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